

# **SUR498H**

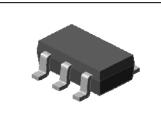
#### Epitaxial planar NPN/PNP silicon transistor

### **Description**

• Dual chip digital transistor

#### **Features**

- Both SRC1203 chip and SRA2203 chip in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



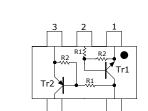
Package: SOT-353

### **Ordering Information**

Type NO.	Marking	Package Code		
SUR498H	X9□	SOT-353		

□ : Year & Week Code

### **Equivalent circuit & PIN Connections**



• Equivalent Circuit

	$\mathbf{R}_{1}$	$\mathbb{R}_2$
Tr1	22ΚΩ	22ΚΩ
Tr2	22ΚΩ	22ΚΩ

#### **PIN Connections**

- 1. COMMON 1
- 2. IN 1
- 3. COMMON 2
- 4. OUT 2
- 5. OUT 1, IN 2

## Absolute Maximum Ratings [Tr1, Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating		Unit		
Characteristic	Symbol	Tr1	Tr2	Cint		
Output voltage	Vo	50	-50	V		
Input voltage	V <sub>I</sub>	40,-10	-40,10	V		
Output current	I <sub>O</sub>	100	-100	V		
Power dissipation	P <sub>D</sub> <sup>∗</sup>	200		mA		
Junction temperature	T <sub>J</sub>	150		150 mV		mW
Storage temperature range	$T_{stg}$	-55 ~ 150		°C		

\*: Total rating

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## **Electrical Characteristics** [Tr1]

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>0</sub> =50V, V <sub>I</sub> =0	-	1	500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =5V, I <sub>O</sub> =10mA	70	120	-	-
Output voltage	V <sub>O(ON)</sub>	$I_0$ =10mA, $I_I$ =0.5mA	-	0.1	0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>0</sub> =0.2V, I <sub>0</sub> =5mA	-	2.1	3.0	V
Input voltage (OFF)	$V_{I(OFF)}$	V <sub>0</sub> =5V, I <sub>0</sub> =0.1mA	1.0	1.2	-	V
Transition frequency	f <sub>T</sub> *	V <sub>O</sub> =10V, I <sub>O</sub> =5mA, f=1MHz	-	200	-	MHz
Input current	$I_{I}$	$V_{\rm I} = 5V$ , $I_{\rm O} = 0$	-	-	0.36	mA
Input resistor (Input to base)	R <sub>1</sub>	-	15.4	22	28.6	ΚΩ
Input resistor (Base to common)	R <sub>2</sub>	-	15.4	22	28.6	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

## **Electrical Characteristics** [Tr2]

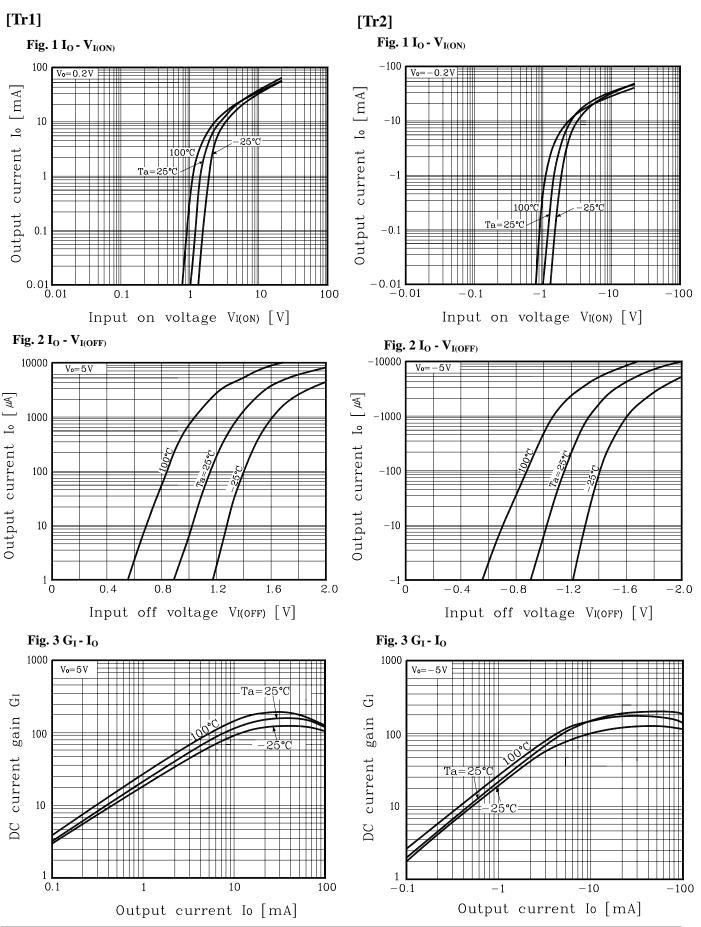
(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>O</sub> =-50V, V <sub>I</sub> =0	-	-	-500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>0</sub> =-5V, I <sub>0</sub> =-10mA	70	120	-	-
Output voltage	V <sub>O(ON)</sub>	I <sub>O</sub> =-10mA, I <sub>I</sub> =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>0</sub> =-0.2V, I <sub>0</sub> =-5mA	-	-2.1	-3.0	V
Input voltage (OFF)	$V_{I(OFF)}$	V <sub>O</sub> =-5V, I <sub>O</sub> =-0.1mA	-1.0	-1.2	-	V
Transition frequency	$f_T^*$	V <sub>O</sub> =-10V, I <sub>O</sub> =-5mA, f=1MHz	-	200	-	MHz
Input current	$I_{I}$	$V_{\rm I}$ =-5V, $I_{\rm O}$ =0	-	-	-0.36	mA
Input resistor (Input to base)	R <sub>1</sub>	-	15.4	22	28.6	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	15.4	22	28.6	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

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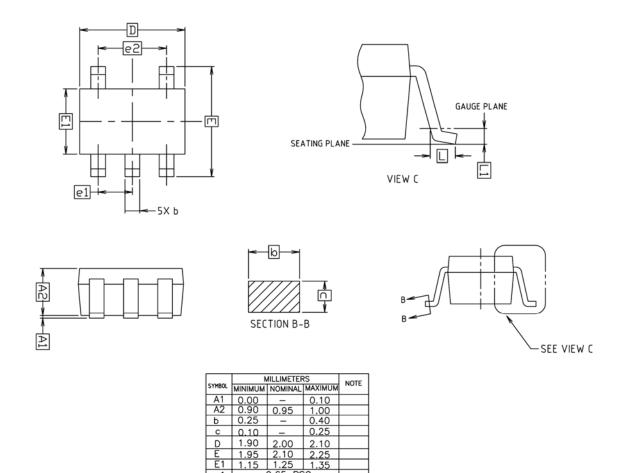
### **Electrical Characteristic Curves**



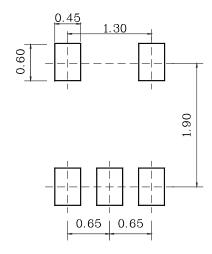
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## **Outline Dimension**



#### \* Recommend PCB solder land [Unit: mm]



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